Mechanical dimensions:

-Correct overall dimensions. Placement of the connectors
-Correct location and dimensions of the cooling plate holes
-Edge Plating

Electrical Connections:

Follow the different DTMROC and ASDBLR chip inputs

DTMROC Inputs:

Starting from Pin # 1, clockwise:

TR14 – 15 P,N:	threshold scan
tp_bias	??
tp_even, TP_off	test pulse scans
th_TR0, th_TR1	??
th_D0, th_D1	threshold scans
Config_select	?
Shaper_select	shape test pulse injection
ASDBLRpwrsense	?
ASDBLRpwrrjmp	?
SpareInpsense	?
spareInpjmp	?
enable_decoup2	?
cmd_in_neg, cmd_in_pos	threshold scan
bc_neg, bc_pos	threshold scan
hard_reset_B_neg,….pos	?
cmd_out_pos, com_out_neg	register readout
data_out_net, data_out_pos	threshold scan
enable_decoup1	
TRST,TCK,TDI,TMS,TD0	JTAG?..what are we doing with these?
Addresses	threshold scan
TR00 – 01 P,N	threshold scan
TR02 – 13 P,N	threshold scan

ASDBLR

Power: VCP,VCS,VES,VCD,
VED,VEDR

Ternary Inputs:	finger test, noise with prot board
Ternary Outputs:	threshold scan
PVCDS	stuffed for what current?
PTH_D	threshold scan
PTH_TR	
XEL, PADJS1,PADJS2	?

BLBIAS	
TST_O	test pulse
TST_E	test pulse
PEN_SH, PEN_BL	
MON_A1,MON_B1	
MON_A8, MON_B8	