

October 27, 2004 Barrel electronics meeting minutes

agenda items:

- 1) update on designs
- 2) new threshold scans from the test beam
- 3) update from SR
- 4) update from PENN

Update on designs

AR3B: Mitch looked over the layout with Nandor and there are no apparent problems with the positions that were in question last week

AR3F: Nandor sent corrections to Bjorn via email and expects a new revision by friday. He is optimistic that this is one step away from final.

AR2F: a bid package has been submitted and CORTEK is putting together a quote right now.

AR3B: the BOM has been verified and is being reviewed by ACAMAS. a stuffing quotation is being processed.

new threshold scans from the test beam

Mike Hance took advantage of an access over the weekend to make some new threshold scans. they can be found under http://www.hep.upenn.edu/atlas/testbeam/october_27.html .

update from SR

of the 4 AR2B pre-series boards that were broken as of last week, 3 were traced to simple stuffing/soldering errors. of these, two were fixed and one is being sent back to ACAMAS (with a poorly-stuffed DTMROC). the other has an undetermined problem with an ASDBLR (possibly a stuffing problem) and is being sent to PENN. The modified AR1BL board from Mitch has been studied and it was determined that adding a copper strip to the ASD side of the board near position 8's NAIS connector can greatly reduce the clock noise. in addition, removing some filter capacitors from the DTM side (between positions 8 and 2) has a beneficial effect as well. It was decided that we will add the copper strip to the 1BL boards that already have a cooling plate mounted, and add the strip and remove the capacitors on those that have not.

update from PENN

One the type-1 board repair front, Mitch has gone through 8 boards in the past week, 6 of which are with godwin for repair. a further determination will be made after the results from those boards are seen. there are still problems bringing up the board test software at PENN, owing largely to problems with the single board computer.

Paul will be working on fixing this before he comes to CERN next week.