

Minutes of the July 07, 2004 TRT Barrel Electronics Meeting  
(Action items on Red)

1. Module Type-I boards. Status of shipping and burn-in plans

Rick: in communication with ACAMAS they said the boards will be shipped this Friday at the earliest.

Philippe: in an informal contact with the ACAMAS people they told him that they broke the tool to test the connectors.

General: agreement to hold at CERN enough boards to instrument 5 modules, and send the rest to NBI. Ben and Carl are preparing the cables.

2) Preparation of PRR for Module Type 2

Philippe: the PRR for Module Type 2 should proceed at the same level as for Module Type 1

Rick: Are all the documentation in EDMS?

Nandor: we should check. It will take one day.

Rick: it is conceivable that by next wee we will have enough information to schedule a PRR. A review of all the material could be ready in one week (Mitch and Nandor agree). Some points still to be check on the board like SHAPER select mode and DTMROC temperature and voltage read back. This should be done for 2FS, 2FL and 2BS, 2BL boards.

Mitch: we have 2FS boards at Penn, and the cable for the 2FL. Benn has to ship the 2FL to Penn. Tests on the 2FS/2FL will be done at Penn. Measurements with the 2BS and 2BL will be done by Toni in the test beam.

Next Week:

Preparation of the PRR

Mitch and Nandor: part lists and EDMS documentation

Toni: checklist. Identify all the missing pieces and prepare the check list “a la type 1”

3) Update on Test Beam Performance.

Toni (Mike gone to the test beam area to solve a high voltage short). We have all the boards for two sectors fully working (we can address all the DTMROCs) with a few ASDBLR and single channels not working here and there (supposed to be defect of the boards). A lot of work was done during the week to fix defective data and control lines (2BL1 and 3BL1 down sector that was missing has been recovered). A new 3BL board was received this morning, and has been plugged. 3BL1 is fully operational. 3BL2 can not be read out, because a problem that is supposed to be either in the harnesses or in the patch panel (the lines for that board were never tested). We think it will be easy to solve. Also, we can not power all the boards at the same time, because on the Low Voltage cables is making the power supply to trip. Currently the board 3BS2 (out of the beam) is with out power.

A new power cable should be built. In the next access 3BL1 will be check and try to be fixed.

5) Preparation and work force for SR installation tests.

Ben and Carl had been working mostly on it. Carl has been doing the cables, while Ben has started to work on the software. He needs help. The three main issues to be addressed:

- 1) Implement Connectivity Tests
- 2) Store the data in a data base
- 3) Take a decision on which data should be stored in the data base.

Gabe can give a hand on the data base. Toni will help on all the three points, once the test beam becomes a bit more stable. It is better to go ahead and implement something (tests, store in data base, and decide preliminary which data to store) and later come back for discussion.

Interlocks: We do not know what interlocks are now implemented. We want to have at least the test beam interlock (software: DCS reads the temperature and shots down the low voltage).

Rick: we want also a hardware interlock.

Harold: could be done with the refrigerant flow or temperature.

Toni: we should contact John Callahan. He had some ideas how to do it.

Fido: we should investigate the scheme implemented by the SCT. Philippe knows (he was the reviewer).

Have the software Interlock implemented.

Toni will talk to John about the hardware schemes.

Ole will talk to Philippe about the SCT scheme.

## 6) Other businesses:

Fido: question about the paddle cards. When we can send the information to Dubna, so they can start? General and somewhat confusing discussion follows. For the next week initiate the process to have a mini PRR for the paddle cards.