

## TRT Barrel Electronics VC Minutes from February 16, 2005

### Agenda Items:

- Status of boards (hopefully including preliminary AR3B diagnosis)
- parts and plans for 6 module test
- any miscellaneous items

### Status of Boards:

AR3B: background: the pre-series was received from ACAMAS on february 7 and tested. There were 2 panels that seemed good and 2 panels that had numerous ASDBLR failures. most of these were full chip failures that were hard to understand. They were sent to PENN for investigation.

Report: Mitch removed some chips and found them to be failing. Mike reports that the sorting machine must have picked failing chips for this lot. The scope of the problem is thought so far to encompass 3 lots that were sent to ACAMAS in july, so the chips that were recently sent to ALGEN were probably not affected. There will be more investigation here in the next few days.

AR2F: now on hold until we understand the scope of the chip sorting problem.

AR3F: the Bill of Materials is now good. Rick will ask for a quotation soon.

### Parts and Plans for 6 Module Test:

the second patch panel set is "coming up" at PENN. It will be 2 weeks at the earliest for a front harness to be ready. In total, there are 4 patch panel sets in existence (enough to read out both sides of 6 modules) but there is a desire to save some to do tests at PENN, so PENN will have an internal discussion and decide how best to distribute resources.

Nandor got a quote of \$500 for 50 LV regulator boards (2 week turn). There is 1 week that is expected for assembly at PENN of the first batch to be sent to CERN (16 boards). There has been no response after 2 weeks now from the CERN PCB shop, so that avenue is considered dead.